

**PATENT APPLICATION**

**RESPONSE UNDER 37 CFR §1.116  
EXPEDITED PROCEDURE  
TECHNOLOGY CENTER ART UNIT 1756**

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Hiroshi SERA et al.

Group Art Unit: 1756

Application No.: 10/630,861

Examiner: C. SULLIVAN

Filed: July 31, 2003

Docket No.: 116741

For: METHOD OF MANUFACTURING RESIN INSULATING LAYER, SUBSTRATE  
FOR ELECTRIC OPTICAL APPARATUS, METHOD OF MANUFACTURING  
ELECTRIC OPTICAL APPARATUS, AND ELECTRIC OPTICAL APPARATUS

**REQUEST FOR RECONSIDERATION AFTER FINAL REJECTION  
UNDER 37 CFR §1.116**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In reply to the October 2, 2007 Final Rejection, please consider the following:

**Remarks.**